Amendments to the Specification

Please replace the paragraph at page 1, lines 3 through 4 with the following amended paragraph:

A1

This application is a continuation of U.S. Patent Application No. 08/866,619 filed May 30, 1997, which issued as U.S. Patent No. 6,324,623 on November 27, 2001, the entire teachings of which are incorporated herein by reference.

Please replace the paragraph at page 33, lines 3 through 12 with the following amended paragraph:

A2

In a multi-threaded computing environment, a shared cache system reduces the amount of redundant information stored in memory. A cache memory area provides both global readable data and private writable data to processing threads. A particular processing thread accesses data by first checking its private views of modified data and then its global views of read-only data. Uncached data is read into a cache buffer for global access. If write access is required by the processing thread, the data is copied into a new cache buffer, which is assigned to the processing thread's private view. The particular shared cache system supports generational views of data. The system is particularly useful in on-line analytical processing of multi-dimensional databases.[[-+]]